

FIG. 1

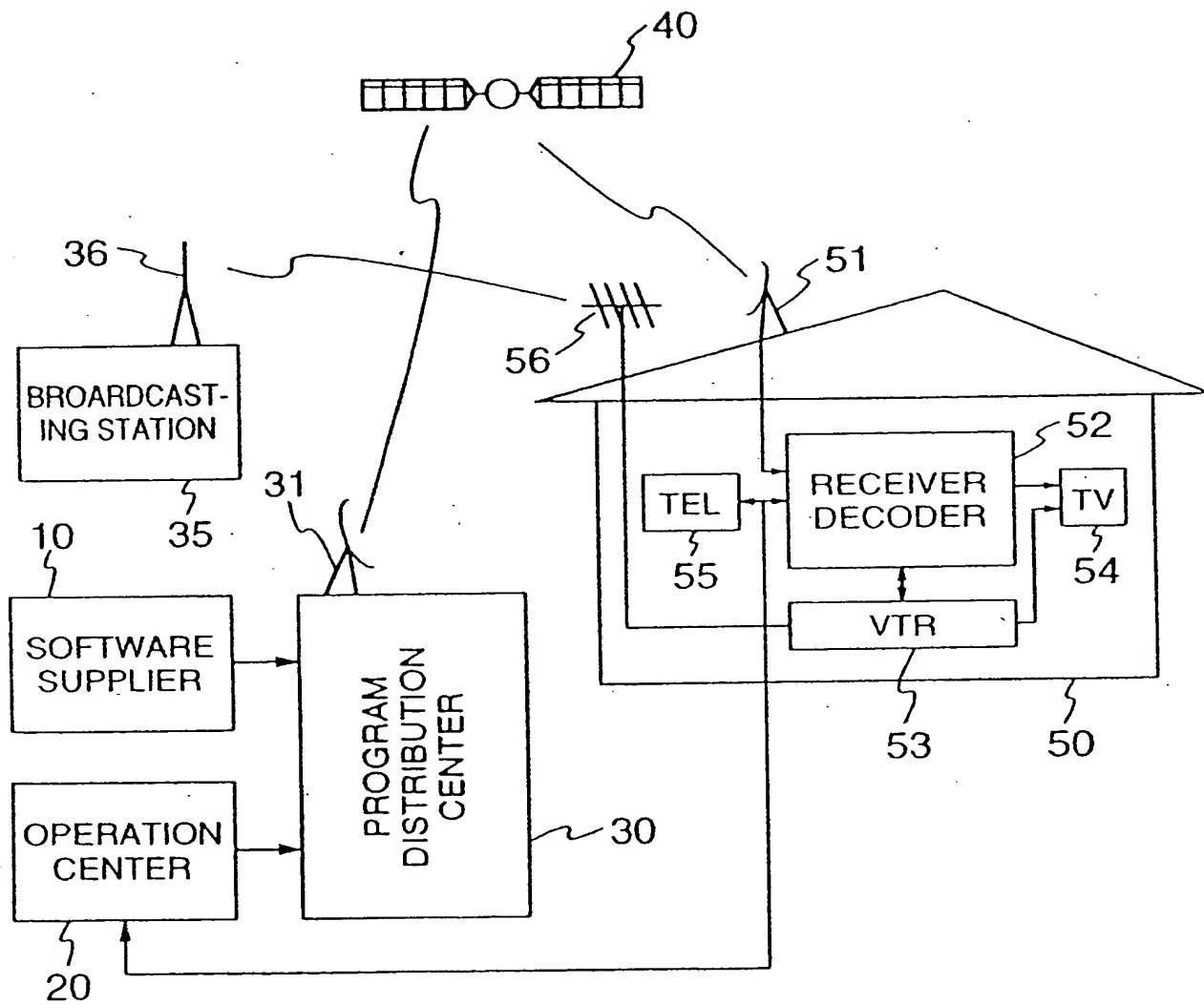


FIG. 2

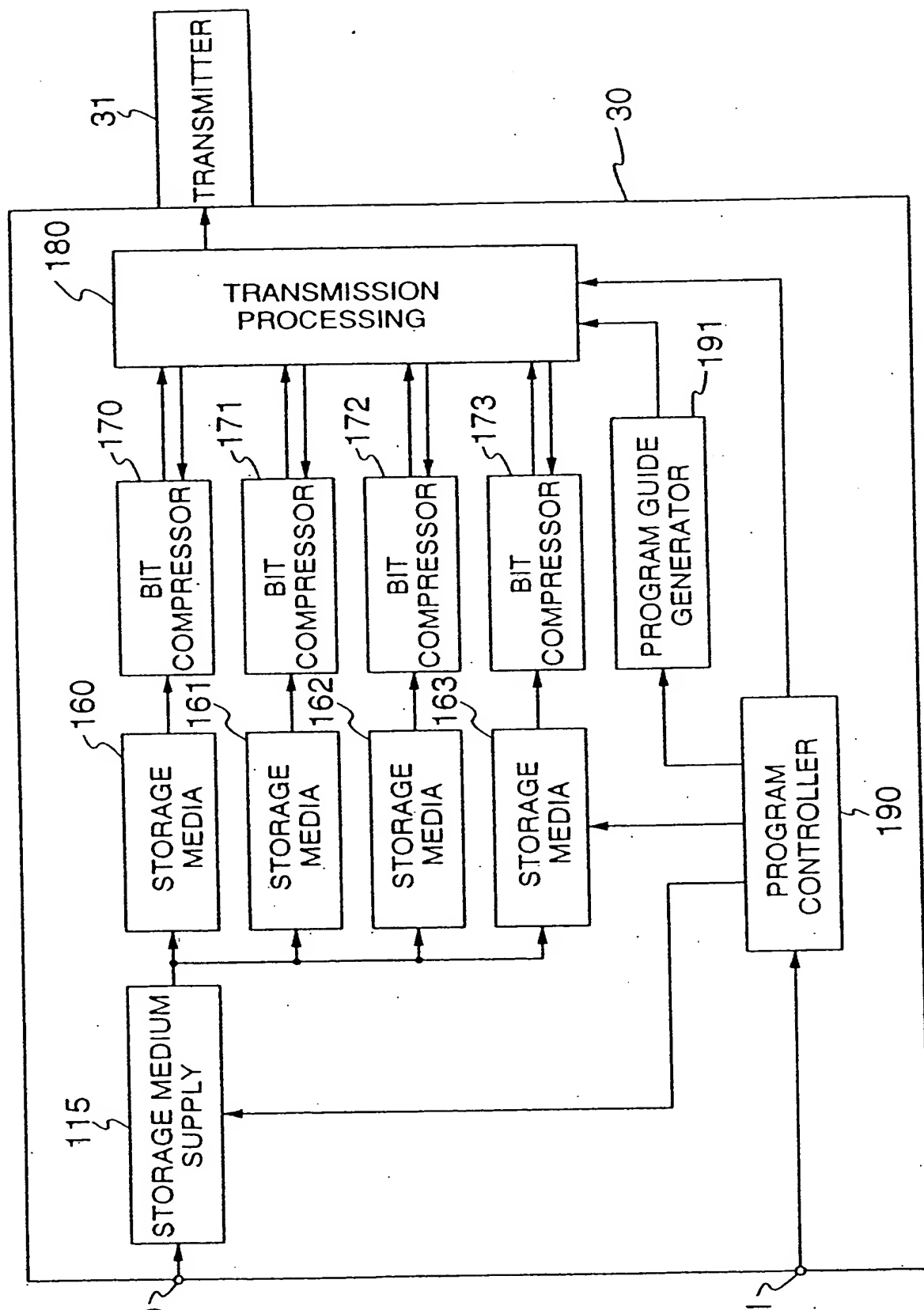


FIG. 3

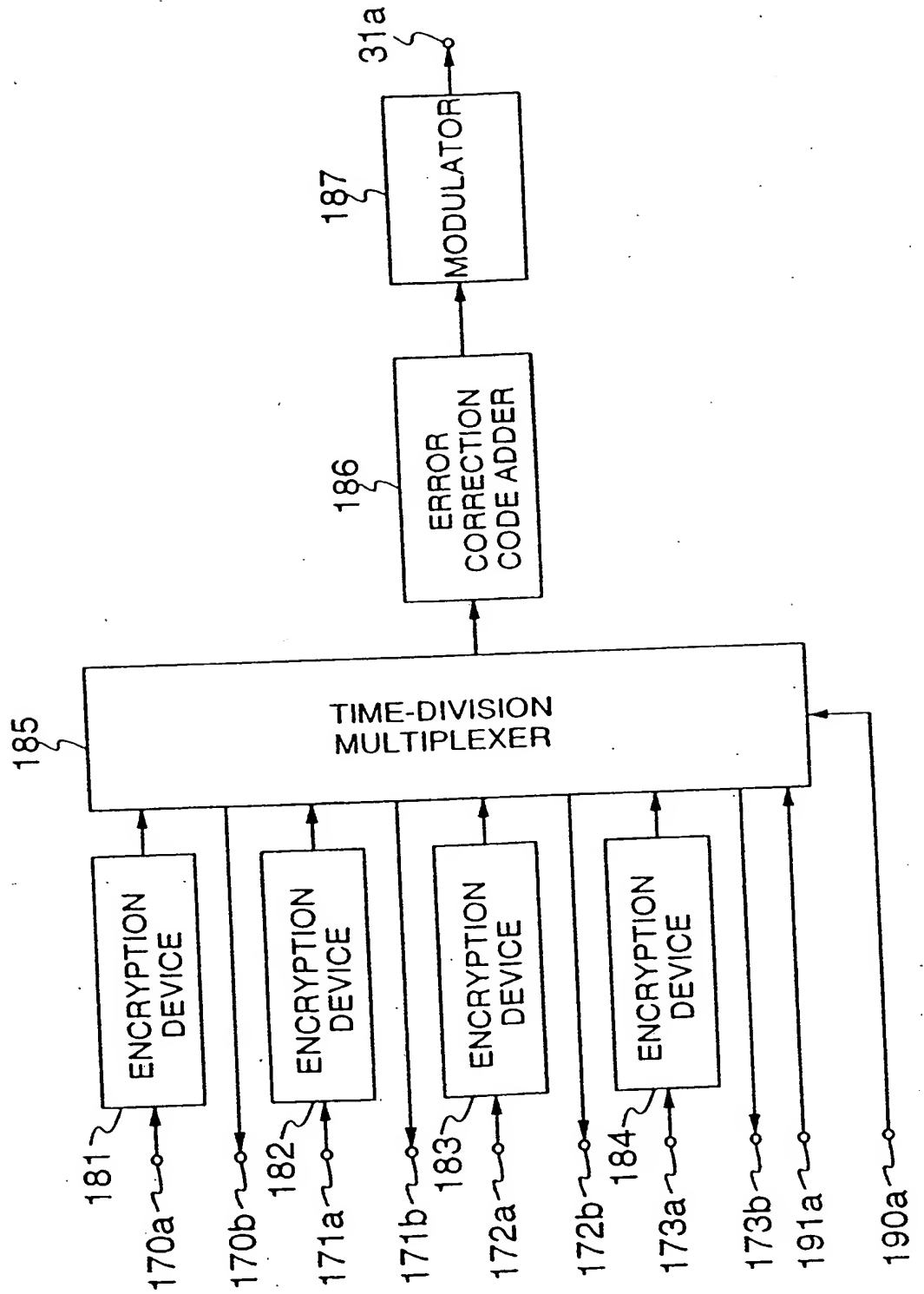


FIG. 4

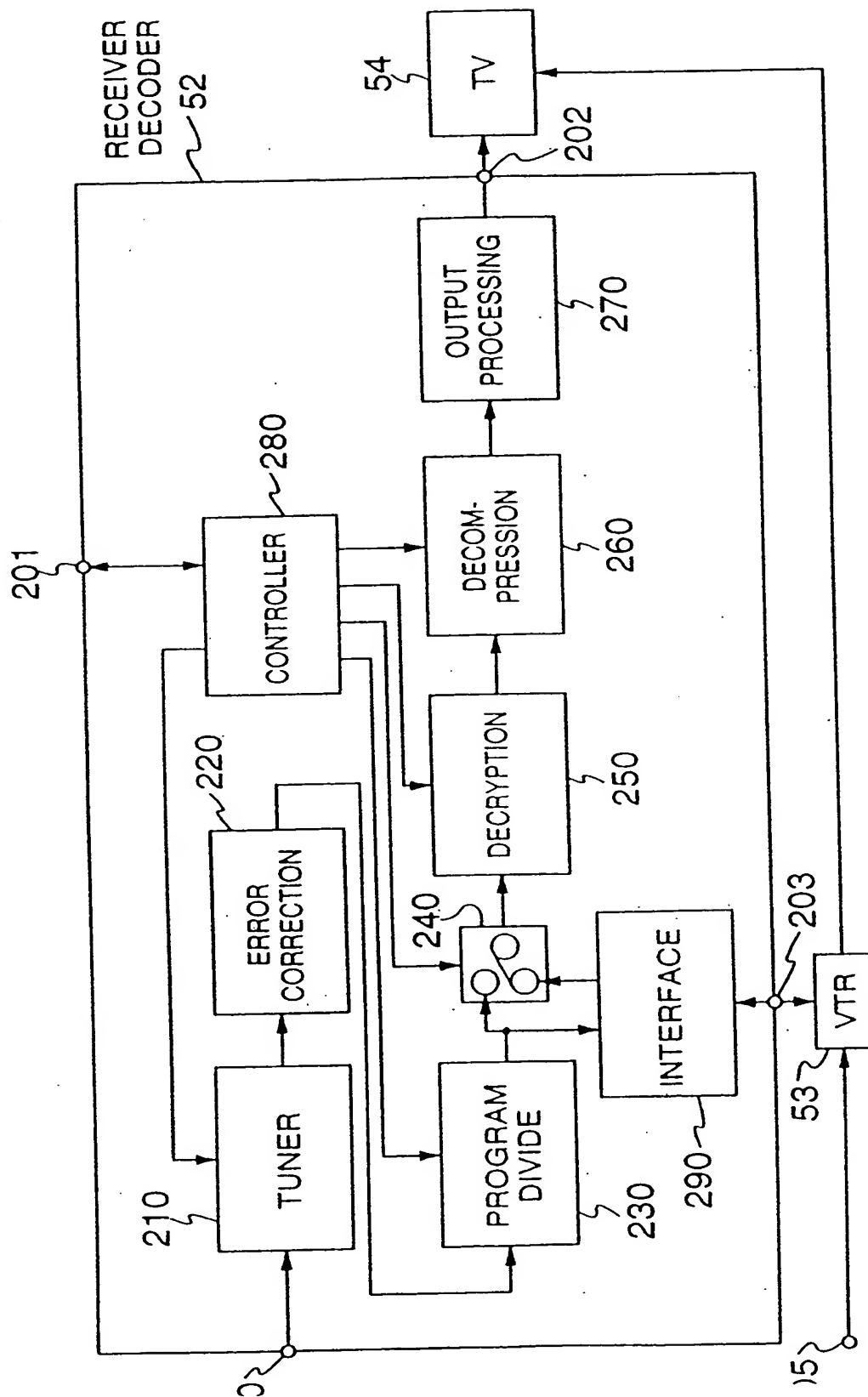


FIG. 5

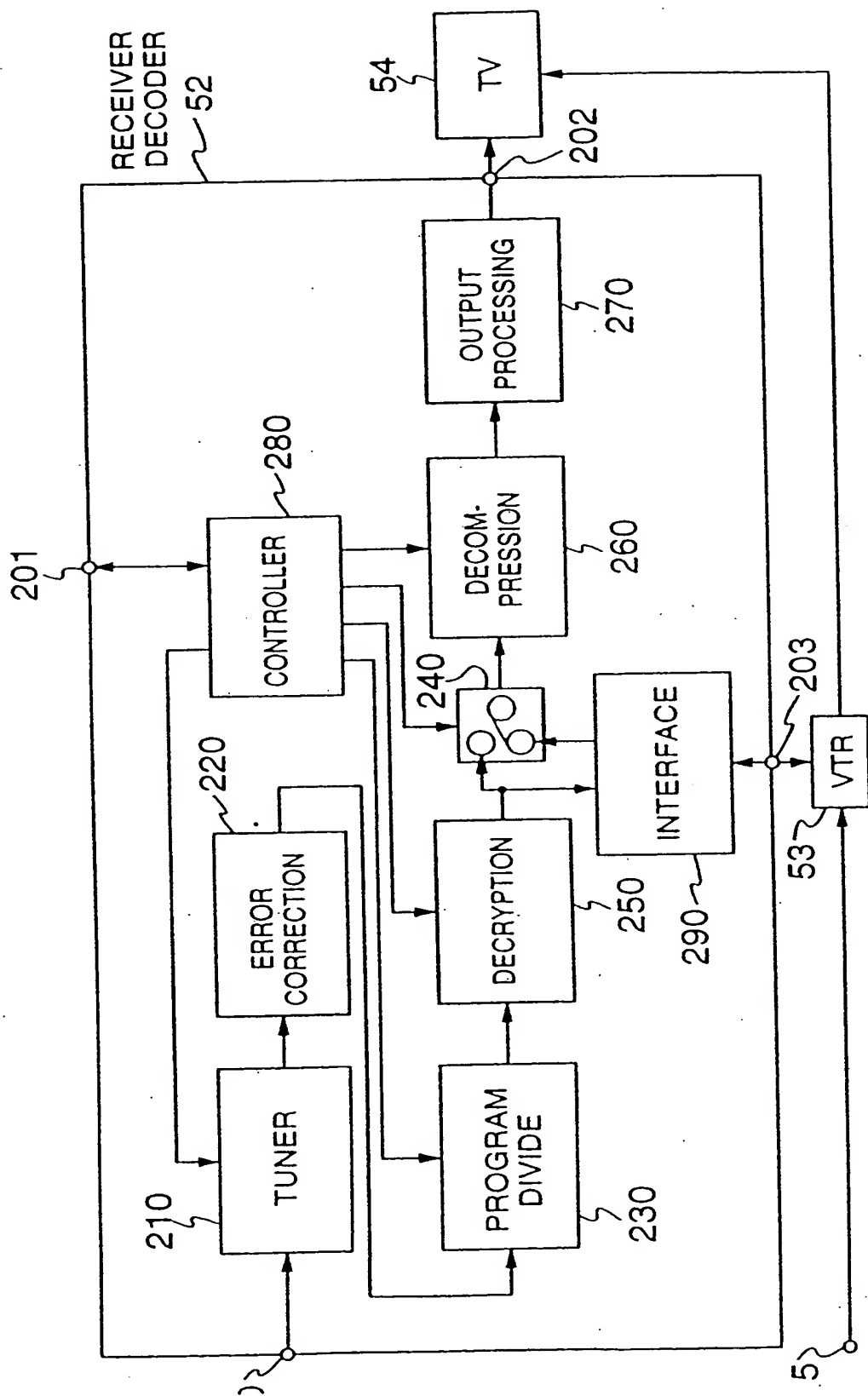


FIG. 6

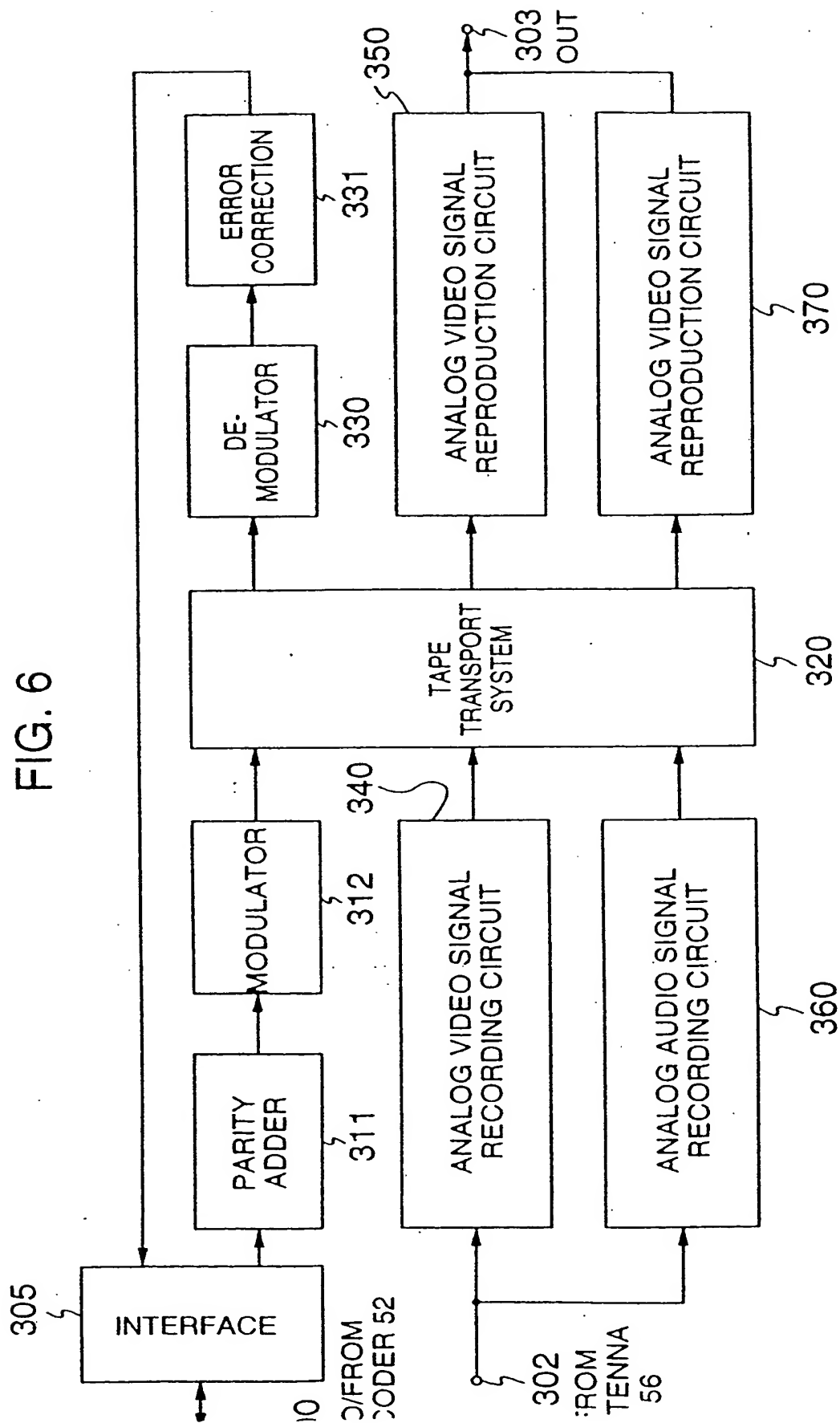


FIG. 7

(1)	V1	V2	A1	V3	PG	V_{ECM}	V1	V4	A2	A_{ECM}	V1	A3	A4
(2)	V4	A2	V1	V3	A1	V4	V1	V4	V2	A4	V3	V4	V4
(3)	A2	PG	V2	V4	V2	V1	V_{ECM}	V3	A3	V2	A_{ECM}	A4	A1
\vdots													
(n)	V1	V2	A1	V_{ECM}	A_{ECM}	PG	V1	V4	A2	V3	V1	A3	A4

FIG. 8

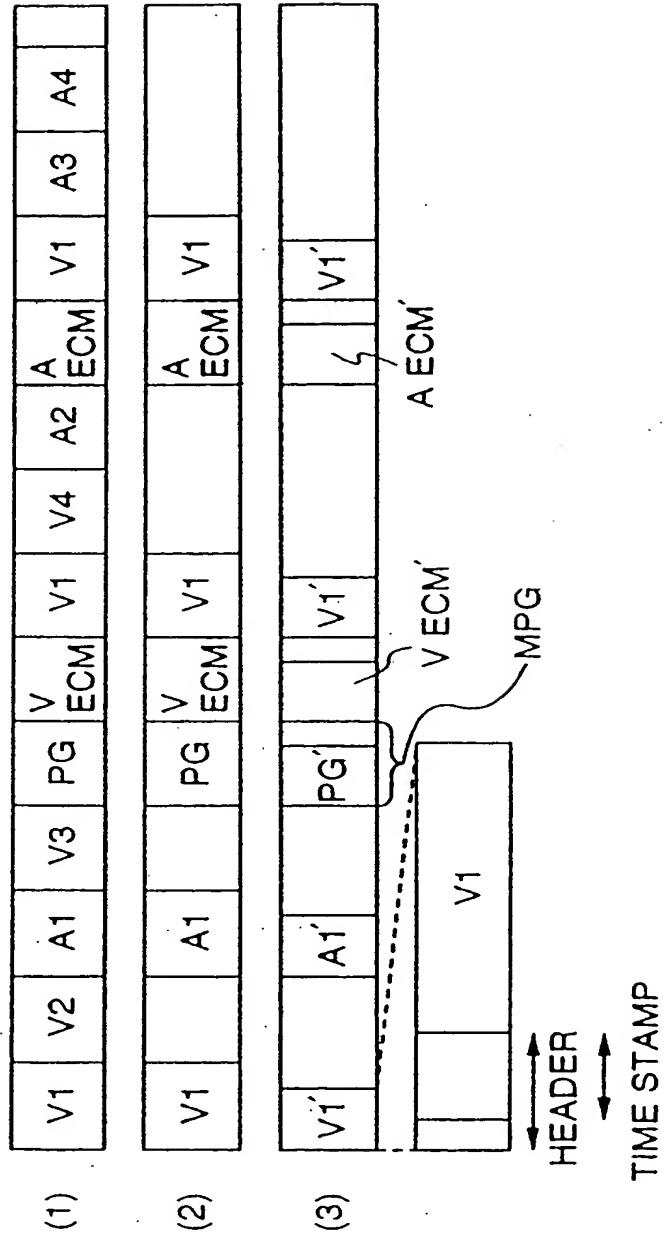


FIG. 9

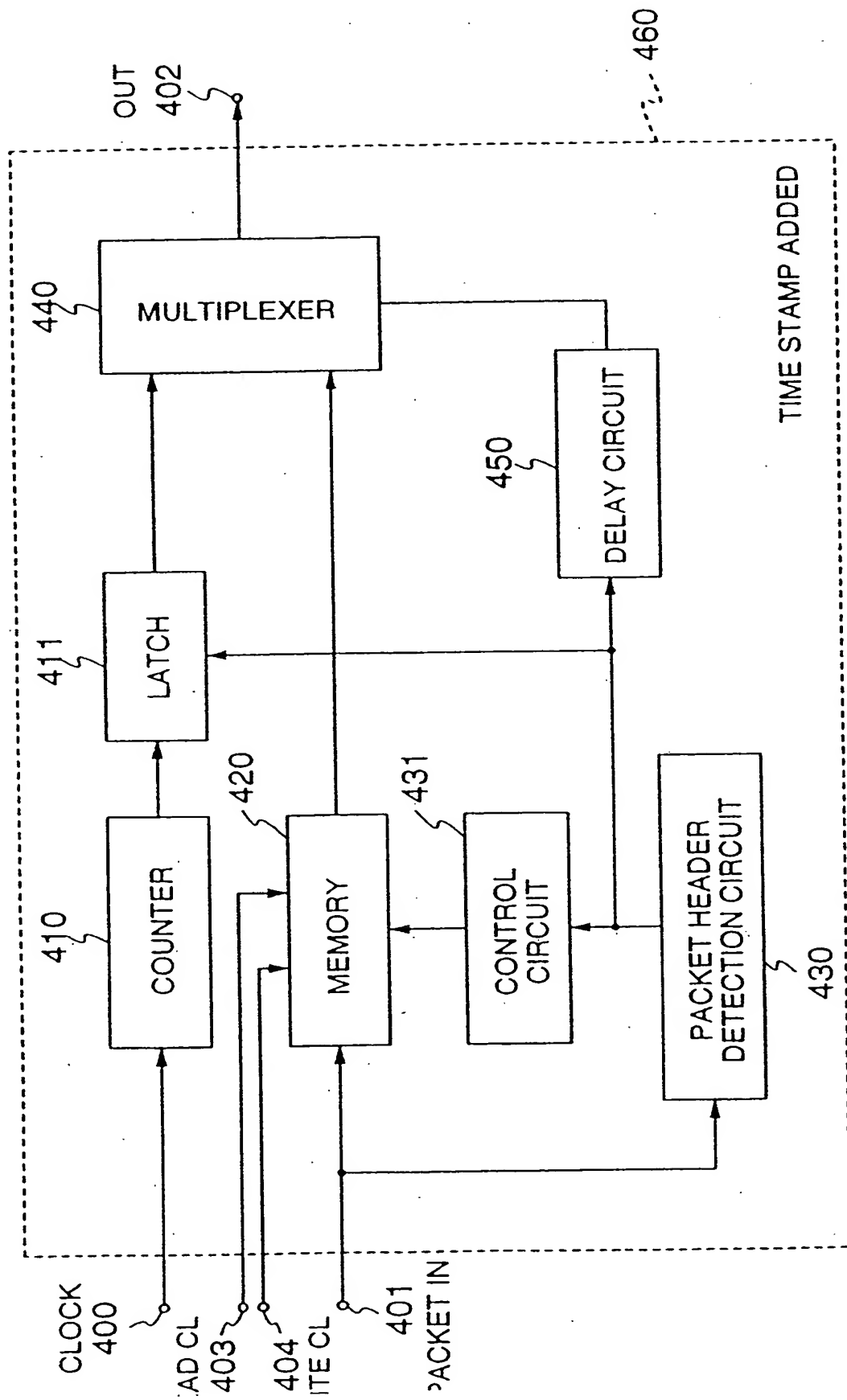


FIG. 10

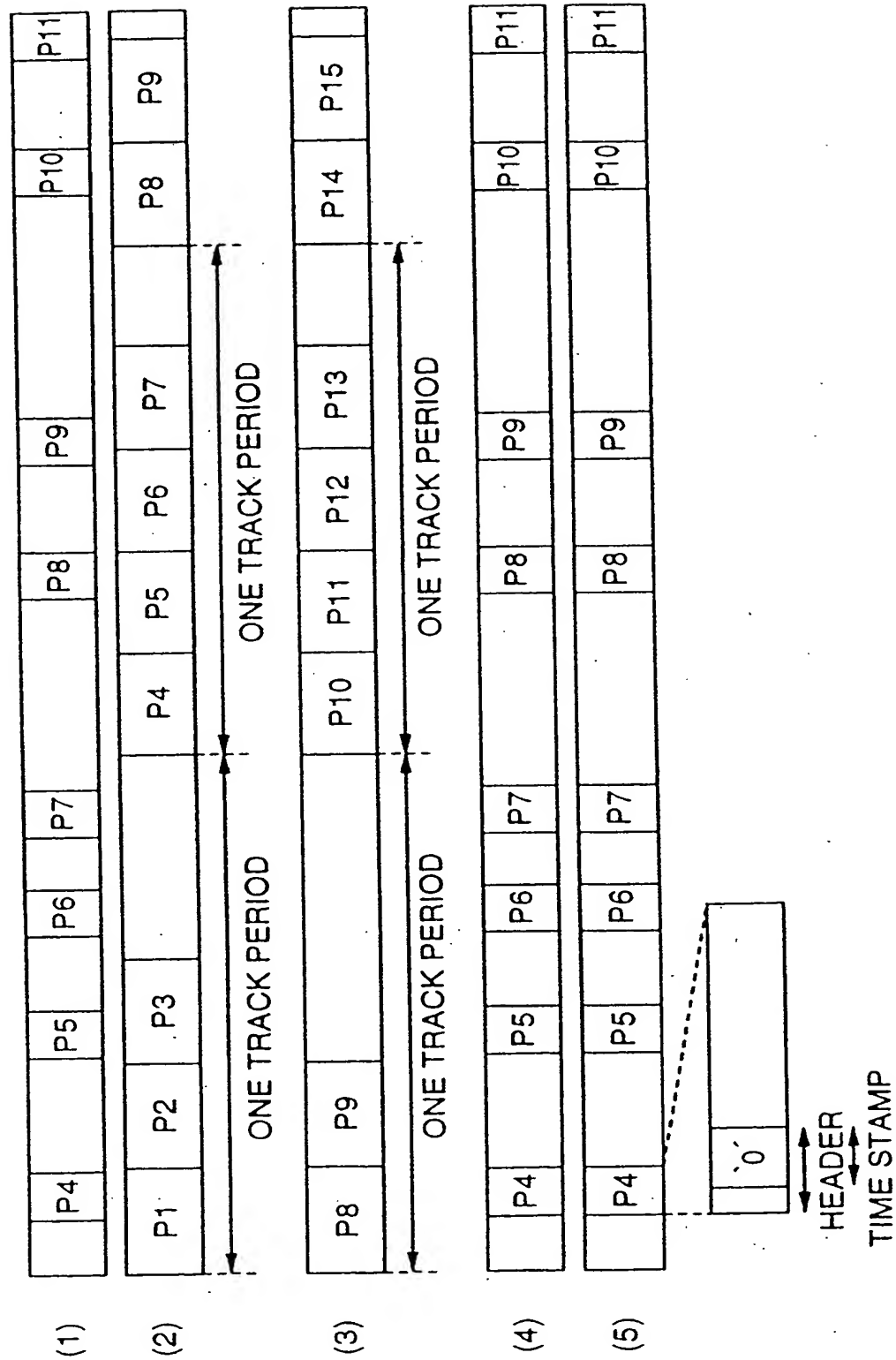


FIG. 11

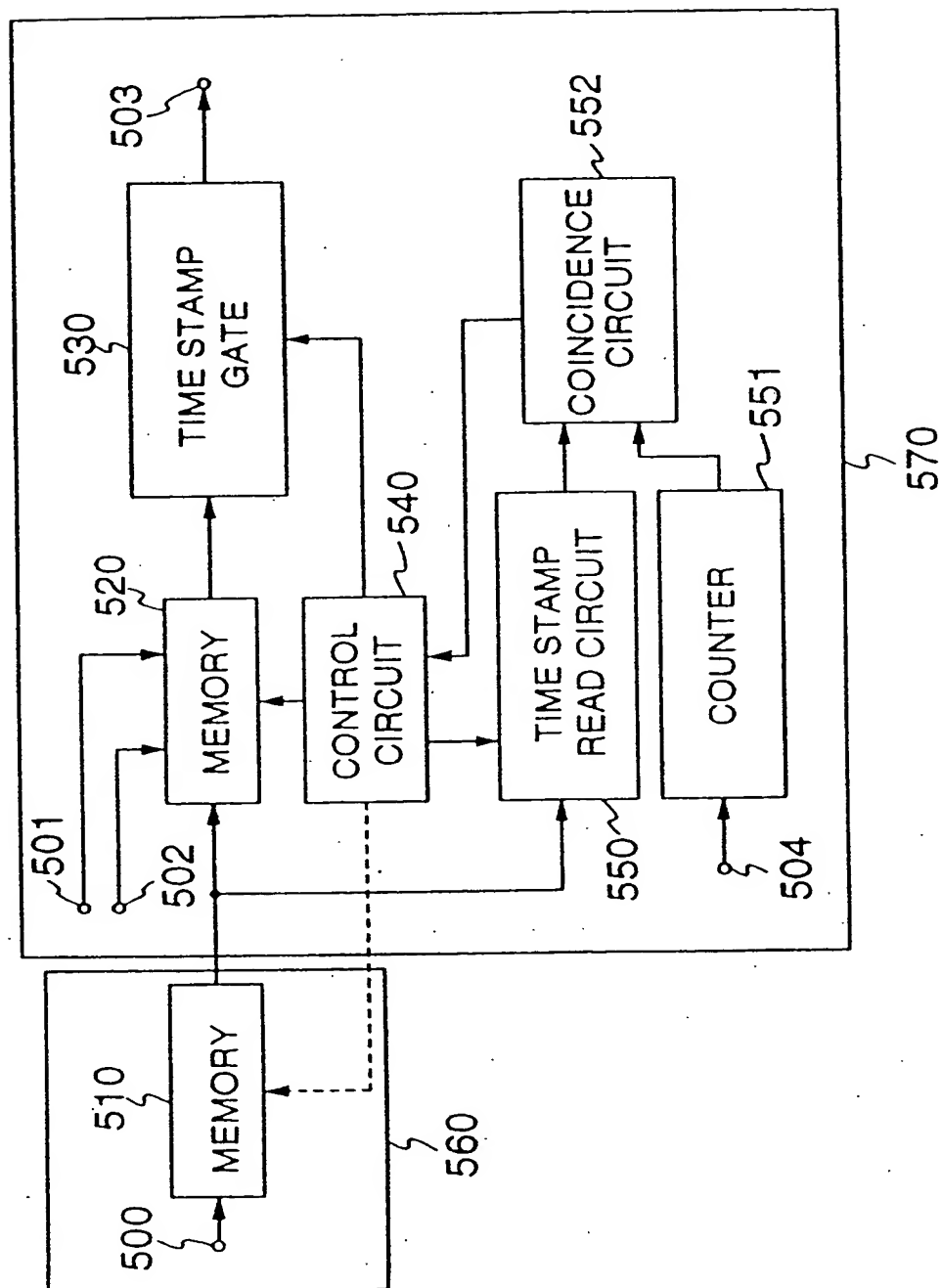


FIG. 12

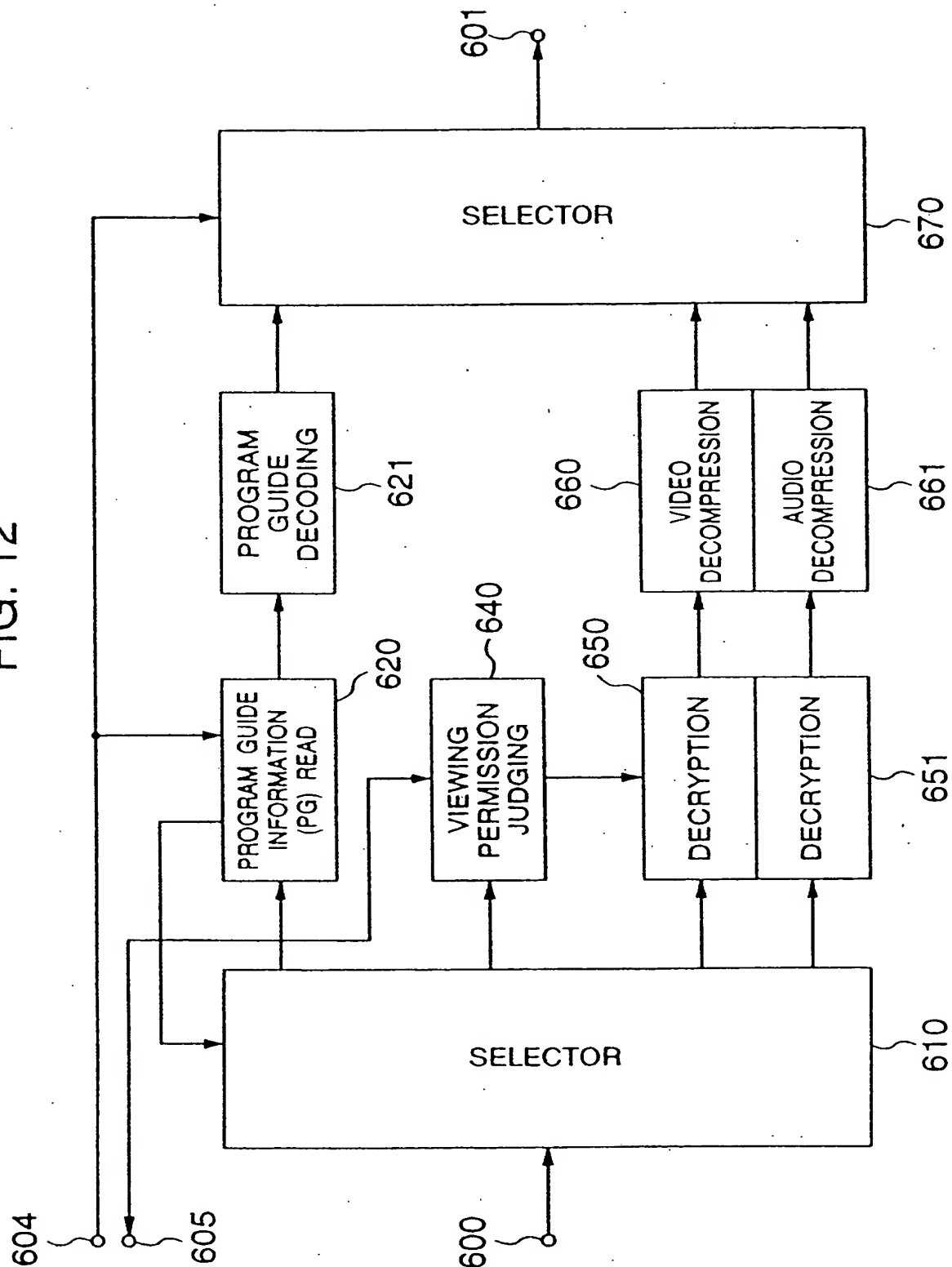


FIG. 13

